

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Tsuyoshi FUJIWARA et al.

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
MANUFACTURING METHOD THEREOF

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any  
assertion as to materiality or prior art effect, the  
document listed on the attached Form PTO-1449 is hereby  
cited.

The document on the attached List is cited in the  
specification, on page 2, and its relevance is indicated  
therein.

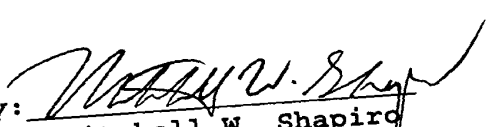
Respectfully submitted,

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February 27, 2002

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